

Advanced OEM Laser Diode Driver with Laser Power Control [LPC optional]



The LDD-1125 is an innovative laser diode driver that contains a specialized current source able to precisiondrive laser diodes in continuous / modulated and pulsed or mixed operation. Equipped with optional laser power measurement circuitry (photodiode input), the LDD-1125-LPC can also be operated as a Laser Power Controller.

Core element of the LDD's internal current source is the generation of highly precise 333 ps timing PWM steps that results in high resolution and very low ripple current.

The output is short-circuit safe and can be chopped for up to 40 kHz pulsed-mode operation. Driving low inductive loads, ultra short fall and rise times are achievable.

For ultimate laser diode protection the supervision of critical system values is directly implemented in hardware. This results in very fast switch-off times (<8 μ s) in case of limit value violation. The LDD-1125 also monitors laser diode temperature (NTC thermistor input).

The LDD-1125 is fully digitally controlled; its firmware is upgradeable to offer various communication options and to meet specific customer requirements.

Current, laser power [LPC option] and temperature measurement hardware can be calibrated.

For basic applications or device evaluation, only a power supply and a laser diode need to be connected to the LDD-1125. The device can operate stand-alone in current control mode, internal generators (on board) allow for parametric definition of flexible output waveforms.

The included PC-Software (USB / RS485) facilitates configuration, control, monitoring and live diagnosis of the LDD. Current and laser power [LPC option] charting is also available from within the software.

All device settings are saved in non-volatile memory and can be backed up and restored.

For remote / OEM applications, the LDD-1125 may be fully controlled by a system bus that features RS485 communication, pulse, interlock and 6 reserve lines.

The LDD-1125 is part of the LDD-Family of Meerstetter laser diode drivers, which are designed to operate alongside devices of the TEC-Family of Peltier controllers. Both families of drivers share the same system bus protocol, design concept and technology.

Features

-HV (High Voltage) Version:

- DC Input Voltage: 12-36 V
- Output Voltage: 0-27 V

Power Stage:

- Output Current: 0-30 A, <2‰ Ripple Parallel operation on request
- Temperature Coefficient: Typ: 20 ppm/K
- CW Current Resolution: 0.6 mA
- Pulse Generation: CW Chopping
- Pulse Rise Time: 135 ns (L_{Load} ~ 10 nH)
- Pulse Frequency: up to 40 kHz

Laser Power Control (LPC): [LPC option]

- CW Laser Power Control: Configurable PID
- Start up phase: Fully parameterizable
- LPC Feedback. PD_{Current}: up to 4 mA

Main Features:

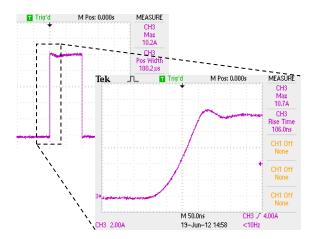
- Internal Generators: Nominal Current, Pulse
- Four Lookup Table with up to 16000 Samples
- Error: Ultra-Fast Switch-off for optimal LD protection
- Configuration / Diagnosis: on PC (via USB / RS485)
- Dimensions (L x W x H): 120 mm x 90 mm x 18 mm
- Efficiency: >94% (@ >50% Load)
- Cooling: over Base Plate

Interfaces

- USB 2.0 1kV isolated (FTDI Chip)
- 2x RS485 / RS422
- NTC for LD Temp.

Digital I/O, 3.3 V / 5 V

- Pulse Input
- Interlock (Enable)
- Generator Trigger Input
- And some more configurable functions





Absolute Maximum Ratings					
Supply voltage (DC)37 V (SV: 27 V)					
Supply current (DC)	25 A (On Board Fuse)				
Output current	35 A				
Output voltage	VIN				

Operating Ratings

1 5 5	
System base plate	< 50°C
Operation temperature	0 – 60°C
Storage	-30 – 70°C
Humidity	5 – 95%, non-condensing

Electrical Characteristics

Unless otherwise noted: T_{A} = 25°C, V_{IN} = 24 V, V_{LD} = 10 V, LDD-1125-SV

Symbol	Parameter	Conditions	Min	Тур	Max	Units
DC Power	Supply Input:					
VIN	Supply voltage		11.5	36	36.5	V
Vin	Supply voltage	LDD-1125-SV	11.5	24	26.5	V
VIN_RIPPLE	Ripple tolerance				300	тV _{PP}
System Ch	naracteristics:					
η50%	Power efficiency	@ 50% load		95		%
η _{90%}	Power efficiency	@ 90% load		93		%

Output Characteristics

Unless otherwise noted: $T_A = 25^{\circ}C$, $V_{IN} = 24$ V, $V_{LD} = 10$ V, LDD-1125-SV

Symbol	Parameter	Conditions	Min	Тур	Max	Units
Output CW					-	
lout	Current range		0		30	Α
T _{coefficient}	Temp. coefficient	$I_{out} = 30A$, $T_A = 25^{\circ}C - 50^{\circ}C$		20	35	ppm/K
IOUT_RES	Current resolution			0.6		mA
IOUT_RIPPLE	Current ripple	l _{out} > 2A		10	50	mA
Vout_max	Diode voltage		0		15	V
Vout_max	Diode voltage	LDD-1125-HV, V _{IN} = 36V	0		27	V
Vout_limit	Output voltage			V _{IN} - 8		V
Pout	Output power	$V_{LD} = 15 V$			450	W
fcw	Current change	For L _{Load} <100 nH, higher f _{CW} are possible		3		kHz
IOUT_SLOPE	Current slope limit	I _{out} > 10A		0.2		A/us
Output Puls	se:					
trise	Current rise time	$L_{Load} \sim 10 \text{ nH} / V_{LD} = 2.5 \text{ V}$		135		ns
t _{fall}	Current fall time	$L_{Load} \sim 10 \text{ nH} / V_{LD} = 2.5 \text{ V}$		1200*		ns
t _{delay}	Delay pulse/current	$L_{Load} \sim 10 \text{ nH} / V_{LD} = 2.5 \text{ V}$		800		ns
t _{pH_min}	Minimal Pulse High	$L_{Load} \sim 10 \text{ nH} / V_{LD} = 2.5 \text{ V}$		1		us
t _{pL_min}	Minimal Pulse Low	$L_{Load} \sim 10 \text{ nH} / V_{LD} = 2.5 \text{ V}$		3		US

* Given by the discharge energy.

Safety Characteristics

Unless otherwise noted: $T_A = 25^{\circ}C$, $V_{IN} = 24$ V, $V_{LD} = 10$ V

Symbol	Parameter	Comments	Min	Тур	Max	Units
I/O Ports:						
toff_current	Overcurrent			6	8	μS
toff_opval	Operating Values	Voltages, currents		100		μS
toff_sfail	System failure	System status		100		ms

Laser Diode Temperature Measurement

Unless otherwise noted: $T_A = 25^{\circ}$ C, $V_{IN} = 24$ V, NTC = B _{25/100} 3988K R ₂₅ 10k								
Symbol	Parameter	Comments	Min	Тур	Max	Units		
Temperature Measurement								
R _{NTC}	NTC Resistance			10		kΩ		
TRANGE	Temperature Range		-6		150	°C		
TPRECISION	Temp. Precision	Not calibrated			1.5	°C		



General Purpose Digital I/O Characteristics on X3 (RES1 ... RES8)

Unless otherwise noted: $T_A = 25^{\circ}C$, $V_{IN} = 24 V$

Symbol	Parameter	Comments	Min	Тур	Max	Units
Input Cha	racteristics:	·				
VIH	Logic high input threshold		2.35			V
VIL	Logic low input threshold				0.9	V
VIMAX	Maximum input voltage		-0.3		5.5	V
Van	Input voltage range	Analog input	0		3	V
Output Ch	naracteristics: (RES1 RES4)					
Vон	Logic high output voltage		2.9	3.3		V
Vol	Logic low output voltage			0	0.4	V
Rs	Series Resistor		170	200	230	Ω
Output Ch	naracteristics: (RES5 RES8)					
V _{OH}	Logic high output voltage		2.9	3.3		V
V _{OL}	Logic low output voltage			0	0.4	V
Rs	Series Resistor		1160	1200	1240	Ω
ESD Prote	ection:					
Vpp	ESD discharge	IEC61000-4-2			100	kV

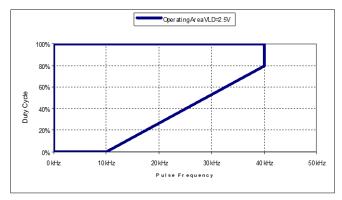
Pulse Operation

However in Pulse Operation mode it is very important, that the inductance of the load is as low as possible. The inductance should be well in the low nH range. A rough rule is that every mm cable adds around 1nH inductance. For optimal performance the following Parameters should be meet.

- L_{LOAD} as low as possible
- V_{OUT} < 0.5 * V_{IN}

Operating Area (Pulse Mode)

Unless otherwise noted: $T_A = 25^{\circ}C$, $V_{IN} = 24$ V, $V_{LD} = 10$ V, LDD-1125-SV



Test Conditions:

$$\begin{split} T_A &= 25^\circ C, \ V_{IN} = 24 \ V, \ I_{LD} = 20 \ A \\ V_{LD} &= 2.36 \ V @20A, \ R_{LD_{Diff}} = 68 \ m\Omega, \ L_{LD} = 10 \ nH \\ Laser diode directly mounted to the screw terminals. \end{split}$$

The maximum achievable pulse frequency depends on the connected load's characteristics. The reason for the performance derating for high frequencies and low duty-cycles (bottom-right part of the operating area) are the internal control topology and available FPGA hardware resource.



Package Outline and Pin Configuration Mounting (M3-size Bores, 7.5mm in Length): M1: x = 4.0 mm, y = 86.0 mm M2: x = 98.0 mm, y = 86.0 mm M3: x = 4.0 mm, y = 4.0 mm M4: x = 98.0 mm, y = 4.0 mm Power Terminals: M4-size Screws LDD Board Pin Descriptions Platform Bus X3: Photo Diode 4 D 1: GND for TYCO Plug 14-POL t 2: RES1 8-215083-4 A 3 $^{\circ}$ 3: RES2 4: RES3 2 D 5: RES4 NTC, LD Temperature 6: RES5 A 1 O 7: RES6 8: RES7 9: RES8 10: GND 11: RS485_A1 [120Ω Termination, DIP-Switch J1, 1] 12: RS485_B1 for TYCO 4-POL 13: RS485_A2 [1200Termination, DIP-Switch J1, 2] Plug Housing 14: RS485_B2 794617-4 with AWG 20-24 Crimp Contacts I 794610-1 X8 (% (+ C4 + C5 + C7 (19 (+ (M2 • M1 (08 C1 C2 C3 1 Power Þ 3 (x1) + X8 Temp. Mea LPC Meas. Supply + È 2 4 D1 VIN \Box 4-POL Connector. cis Back View (Cable L2 L3 Н1 Insertion Voids) (X2) (X6) GND Cathode L5 X3 PBC H3 L6 DN STM32 Status-LEDs Laser J1 Diode (X5) Г L8 Anode U8 H4 R30 L9 0 L10 L11 FPGA Status-LEDs X**4** Mini-USB U14 y OTP2 OTP3 \Box H5 • M4 M3 П Dimensions (L x W x H): 120 x 90 x 18 mm³ (0,0)х

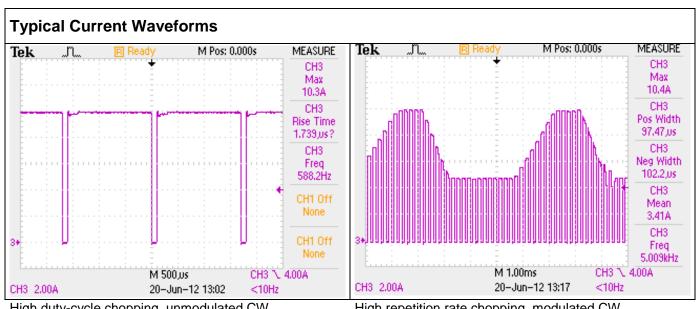
Laser diode, temperature probes, power supply and connectors not included.



Current-Controlled Operation-Modes and Communication Option

The LDD-1125 is an OEM high performance current source that is primarily designed to operate as a fast pulsed laser diode driver but that can also be used in CW mode. It is configured over an industry-standard RS485 or a USB connection, either GUI-driven using the included LDD Service Software, or by direct parameter control using the predefined communication protocol. Basic system status is visually indicated by on-board LEDs, more detailed status information can be polled at any time. The LDD-1125 can operate in a stand-alone configuration as well as in a remotely-controlled manner, with parameters adjusted on the fly. The laser diode driver is current-PID-controlled.

Configuration parameters further include: control source selection, maximum current limits, nominal current ramping, PID controller settings, NTC temperature sensor modeling coefficients, measurement circuitry calibration, error thresholds, communication watchdog, etc. Please refer to the user manual for further information.



High duty-cycle chopping, unmodulated CW.

High repetition rate chopping, modulated CW.

LDD Service Software The included LDD Service Software is a powerful tool that ¥ 1.00 allows monitoring and full configuration of the LDD-1125 via a standard USB or an RS485 connection from a PC Current CW [A] urrent High (A) running Windows. Current Low (A This tool is ideal for laboratory setups, product evaluation High Time (us Low Time (us) diagnosis, debugging and commissioning: Rise Time (us) Fall Time fus 1000 internal generators set up [see. illustration on the left] configuration import and export data charting with trigger functionality High Time (u: error codes and built-in descriptions hardware configuration (e.g. calibration) maintenance (e.g. firmware upgrades) Please refer to the laser diode driver user manual for more Import Config Read Config Login Use Reset LDD Export Config Write Config information on features and system requirements.

CW Current Modulation and Chopping by Internal Generators.



Laser Power -Controlled Operation-Mode [Devices with LPC Option only]

The LDD-1125-LPC is a laser power controller that is based on the LDD-1125, with additional light measurement circuitry (photodiode input). A user-defined 'Light System Scale' factor links the generated photocurrent to the absolute light power. The light PID controller's output is fed to the current controllers input. The nominal light power value may be CW, modulated CW (using internal generation) or remotely controlled. A configurable soft-start feature is available, as well.

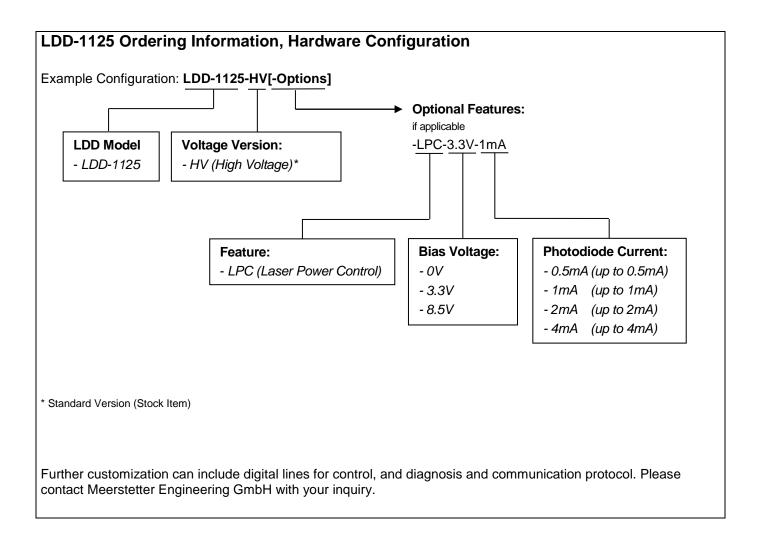
Light Measurement Characteristics [Devices with LPC Option only]

Unless otherwise noted: $T_A = 25^{\circ}C$, $V_{IN} = 24 \text{ V}$, $V_{BIAS} = -3.3 \text{ V}$

Symbol	Parameter	Comments	Min	Тур	Max	Units	
Photodiode Input Characteristics:							
IPD	Photodiode current				4	mA	
fadc_lpc	Sampling frequency	@ 16bit		0.5		MSps	

Typical LPC Waveforms 🕰 LDD Service Software CDD Service Software ¥ 1.10 ¥ 1.10 - 🗆 🗙 Monitor Operation Control Light Control Chart Settings Maintenance Monitor Operation Control Light Control Chart Settings Maintenance Data Acquisition Settings (Device Parameter) Actual Chart Setting Data Acquisition Settings (Device Parameter) -Actual Chart Settings Zoom [%] 100 New New Set Current Time Offset [ms] 100 Set Trigger Source Enable • Time Offset [ms] -30 Trigger Source Enable • 💌 Auto Update 🛛 🗷 Bandwidth Limit Light Sampling Interval [us] 100 Display Data Points 8k Display Data Points 8k Sampling Interval [us] 100 -14 14 3 13 -2.8 13 Current Limiter Ramp (1E-05 A/us) 12 -2.6 12 hermal Amapon mont -2.4 11 11 -2.2 10 10 -Current Limite Start Value (10 A) -2 9 9-R - 1.8 Σ 8 8-.aser Diode Current - 1.6 aser Diode Curr Light Pow 7 7 -1.4 6 - 1.2 aser. 5 5 -1 4 -0.8 3--0.6 2--04 -0.2 0 Lo 200 400 600 200 800 Time [ms] Time [ms] Import Config Read Config Connected Connected Device Status Login Reset LDD Export Config Write Config Connect: Device Status Ready Login Connect: Laser Power Control (blue) and Diode Current (red). LPC Soft-Start Limitation (green)





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