

LDDP-20-70 Pulsed/CW buck-boost laser diode driver



Description

LDDP-20-70 is an OEM driver module for integration designed to supply laser diode strings of multiple single emitters in series. Its unique buck/boost switching topology allows DC/DC operation with load compliance voltage even exceeding the DC input voltage: Standard diode drivers with buck converter require a load voltage for minimum ca. 2 V below the supply input.

LDDP can supply loads from 0 .. 70 V from a DC input between 12..52 V, as long as the input current does not exceed 25 A.

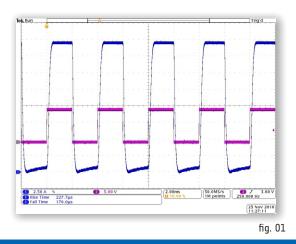
System designers can thus keep using usual e.g. low cost 24 V auxiliary supplies although more and more laser diodes exceed yet the 30 V compliance voltage level.

LDDP-20-70 is fast analog regulated. The up to 99 % highly efficient switching regulation provides pulses or up to 100 % amplitude modulation with typ. 200 µs rise/fall times.

An even faster high speed model (-HS) with typ. rise/fall times $<65 \,\mu s$ and analog modulation to $>10 \,\mu z$ is optionally available.

Besides standard industrial and medical use its low current ripple/noise makes it especially suitable for sensitive pumping applications.

The standard model provides differential signal I/Os for all digital and analog signals.



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Features

- Output current up to 20 A
- Buck/boost $U_{out} = 0 ... 70 V$ independent of U_{in} •
- typ. 200 µs rise/fall time
- Low current ripple/noise <±0.25% pp/0.1% rms
- Efficiency to 99 %
- Especially suitable for fiber laser amplifiers and burn-in systems with multiple single emitter strings

Specifications

Output Rise time Current ripple Current programming Prog. accurracy Monitoring I/U Monitoring accurracy Protective features/ error output	0 20 A / 0 70 V ^{*1]} typ. 200 µs typ. <±0.25% (of full scale) 0 10 V = 0 20 A (2 A/V) typ. <±1 % (of set-point within specified range) 0 10 V (I _{mon} 0.5 V/A, U _{mon} 0.1 V/V, real time) typ. ±0.5% (of set-point within specified range) Monitor starting sequence, soft start, transient protection, 0VP, over temperature, over current, protection shut down reaction time <1 µs Fault = high impedance, ok = low imp.
Control interface	Quasi isolated: Interface GND can float max. ±5 V versus negative input terminal, connector JST 16pin S16B-PADSS-1 Digital interface upon request
Efficiency	typ. 97.5 98.5 %
Input	typ. 48 VDC, allowed range 12 52 VDC *2
Input capacity	1 mF
Environment	-20 °C +50 °C (non condensing)
Cooling	Conductively via baseplate, max. power dissipation 25 W
Baseplate temperature	max. +50 °C
DC connectors in/out	Screw terminals M4
Size (LxWxH)	ca. 120 x 75 x 34 mm

- $^{\star\!1\!]}$ max. output power up to 1000 W. Specified output voltage range 2 .. 70 V, independent of input voltage (U_{in} 24 .. 52 V DC).
- Input current must not exceed 25 A. Specified output range 2 .. 20 A ^{*2)} calibrated standard 24 .. 48 VDC. Operation at 12 VDC (±10%) input possible.
- Consult product management for calibration adjustment

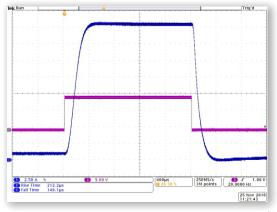


fig. 02

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